



X2P13P22100D192

Cisco® X2-10GB-LRM Compatible TAA Compliant 10GBase-LRM X2 Transceiver (MMF, 1310nm, 220m, SC, DOM)

Product Description

This Cisco® X2-10GB-LRM Compatible X2 transceiver provides 10GBase-LRM throughput up to 220m over multi-mode fiber (MMF) using a wavelength of 1310nm via a SC connector. It is guaranteed to be 100% compatible with the equivalent Cisco® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Skylane's transceivers are RoHS compliant and lead-free.

Features:

- X2 MSA 2.0 Compliance
- Duplex SC Connector
- Commercial Temperature 0 to 70 Celsius
- Single-mode Fiber
- Hot Pluggable
- Excellent ESD Protection
- Metal with Lower EMI
- RoHS Compliant and Lead Free



Applications:

- 10GBase-LR Ethernet
- Access, Datacenter and Enterprise
- 8x/10x Fibre Channel
- Mobile Fronthaul CPRI/OBSAI

For your product safety, please read the following information carefully before any manipulation of the transceiver:



ESD

This transceiver is specified as ESD threshold 1kV for SFI pins and 2kV for all others electrical input pins, tested per MIL-STD-883G, Method 3015.4 / JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module.



LASER SAFETY

This is a Class1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007).

The optical ports of the module need to be terminated with an optical connector or with a dust plug in order to avoid contamination.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Storage Ambient Temperature	T _S	-40		85	°C
Supply Voltage (5V)	V _S	0		6	V
Supply Voltage (3.3V)	V ₃	0		4	V
Supply Voltage (APS)	V _{APS}	0		1.5	V
Optical Receiver Input	P _{IMAX}			1.5	dBm

General Specifications

Parameter	Symbol	Min.	Typ.	Max.	Unit
Data Rate	DR		10.3125		GBd
Bit Error Rate	BER			10 ⁻¹²	
Total Power Consumption	P			3	W
Supply Voltage (5V)	V _{CC5}	4.75	5	5.25	V
Supply Voltage (+3.3V)	V _{CC3}	3.14	3.3	3.47	V
Supply Voltage (APS)	V _{CCAPS}	1.152	1.2	1.248	V
Supply Current (5V)	I _{CC5}			100	mA
Supply Current (+3.3V)	I _{CC3}			300	mA
Supply Current (APS)	I _{CCAPS}			1000	mA
Case Operating Temperature	T _C	0		70	°C

Link Distance

Parameter	Fiber Type	Distance Range (km)
10.3 GBd	9/125μm SMF	10

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Optical Wavelength	λ	1290		1330	nm	
Launch Power	P _{OUT}	-8.2		0.5	dBm	
Launch Power in OMA minus TDP	P _{OUT_OMA}	-5.2			dBm	
Launch Power of OFF Transmitter	P _{OUT_OFF}			-30	dBm	
Side Mode Suppression Ratio	SMSR	30			dB	
Spectral Width (-20dB)	$\Delta \lambda$			0.6	nm	
Optical Extinction Ratio	ER	3.5			dB	
Optical Modulation amplitude	OMA	-5.2			dBm	
Optical Return Loss Tolerance	ORL _T			12	dB	
Relative Intensity Noise	RIN			-128	dB/Hz	
Transmitter Dispersion Penalty	TDP			3.2	dB	
Transmitter Reflectance	TR _{TX}			-12	dB	
Eye Mask Definition		According to IEEE 802.3ae				
Receiver						
Center Wavelength Range	λ_c	1260		1600	nm	
Optical Input Power	P _{IN}	-14.4		0.5	dBm	
Receiver Sensitivity in OMA	P _{IN_OMA}			-12.6	dBm	
Stressed Receiver Sensitivity	P _{IN_S}			-10.3	dBm	
Receiver Reflectance	TR _{RX}			-12	dB	
Loss of Signal Assert Level	P _{LOS_A}	-25			dBm	
Loss of Signal De-assert Level	P _{LOS_D}			-16	dBm	
Loss of Signal Hysteresis	P _{LOS_H}	1			dBm	
Receiver electrical 3dB upper cutoff frequency	FR			12.3	GHz	

Electrical Characteristics - DC

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
A. 1.2V COMS I/O DC Characteristics (PRTAD; LASI; RESET; TX_ON/OFF)						
External Pull-Up Resistor for Open Drain	R _{PU}	10		22	KΩ	
Output High Voltage	V _{OH}	1			V	
Output Low Voltage	V _{OL}			0.15	V	
Input High Voltage	V _{IH}	0.84		1.2	V	
Input Low Voltage	V _{IL}			0.36	V	
Input Pull-Down Current	IPD	20	120	120	μA	VIN=1.2V
B. XAUI I/O DC Characteristics (TXLAN[0..3]; RXLANE[0..3])						
Differential Input Amplitude (pk-pk)	V _{IN_XAUI}	200		2500	mV	AC Coupled
Differential output Amplitude (pk-pk)	V _{OUT_XAUI}	800		1600	mV	AC Coupled
C. MDIO I/O DC Characteristics (MDIO; MDC)						
Output Low Voltage	V _{OL}			0.2	V	I _{OL} = 100μA
Output Low Current	I _{OL}			4	mA	
Input High Voltage	V _{IH}	0.84		1.2	V	
Input Low Voltage	V _{IL}			0.36	V	
Pull-Up Supply Voltage	V _{PU}	1.152	1.2	1.248	V	
Input Capacitance	C _{IN}			10	pF	
Load Capacitance	C _{LOAD}			470	pF	
External Pull-Up Resistance	RPU	200			Ω	

Electrical Characteristics - AC

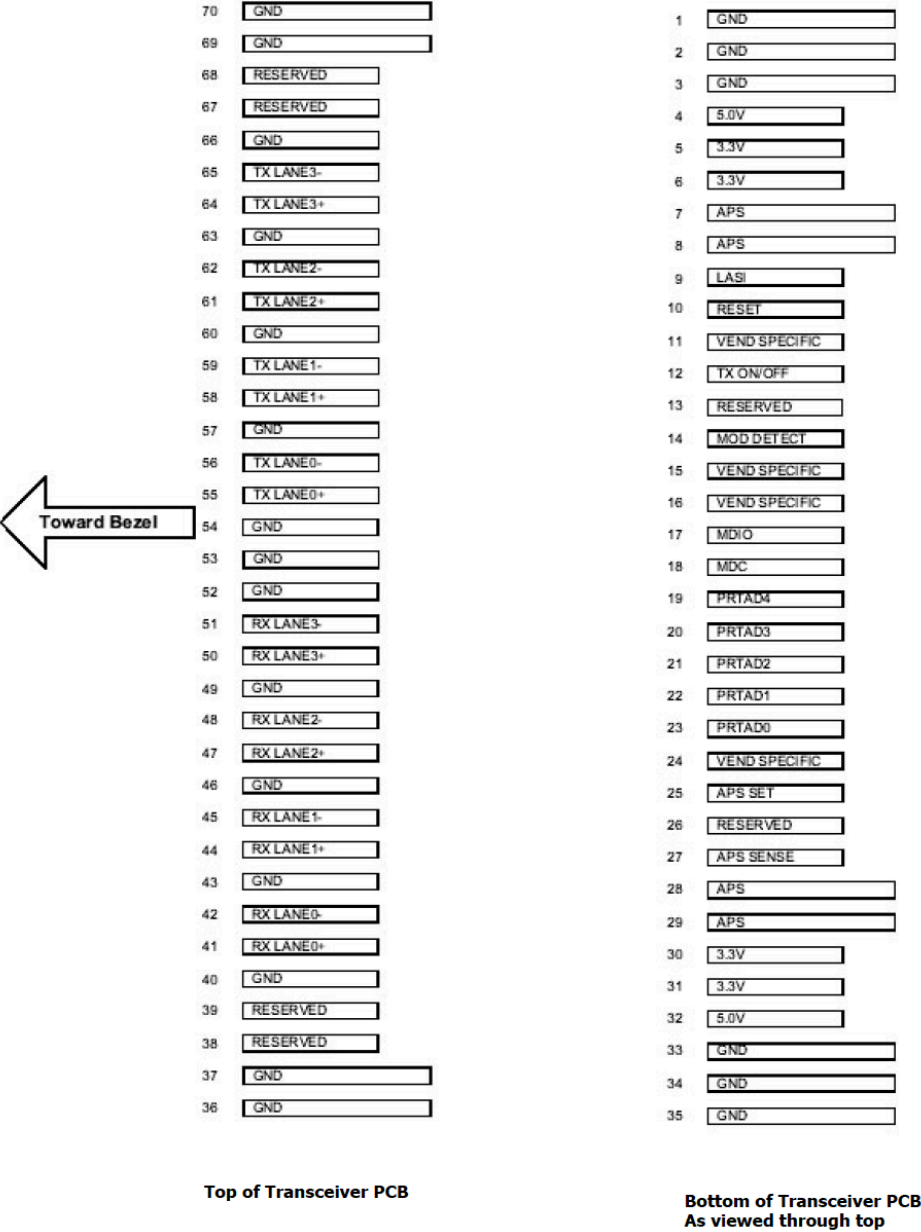
Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
A. XAUI Input AC Characteristics (TXLANE[0..3])						
Baud Rate	BR _{XAUI_IN}		3.125		GBd	
Baud Rate Tolerance	BR _{TOL_XAUI}	-100		100	ppm	
Differential Input Impedance	Z _{IN_XAUI}	80	100	120	Ω	
Differential Return Loss	RL _{IN}	10			dB	100MHz to 2.5 GHz
Input Differential Skew	T _{IN_SKEW}			75	ps	Crossing Point
Jitter Amplitude Tolerance	J _{XAUI_TOL}			0.65	UI _{pp}	IEEE 802.3ae
B. XAUI Output AC Characteristics (RXLANE[0..3])						
Baud Rate	BR _{XAUI_OUT}		3.125		GBd	
Baud Rate Variation	BR _{XAUI_VAR}	-100		100	ppm	
XAUI Eye Mask (far-end)		According to IEEE 802.3ae				
Output Differential Skew	T _{OUT_SKEW}			15	ps	
Output Differential Impedance	Z _{OUT_XAUI}	80	100	120	Ω	
Differential Output Return Loss	RL _{OUT}	10			dB	100 MHz to 2.5 GHz
Total Jitter	TJ _{XAUI}			0.35	UI	Near-end No pre-equalization 1 UI=320 ps
Deterministic Jitter	DJ _{XAUI}			0.17	UI	
C. Power-On Reset Characteristics						
Power-On Reset and TX_ONOFF Characteristics		According to X2 MSA Issue				
D. MDIO I/O AC Characteristics (MDIO; MDC)						
MDIO Data Hold Time	T _{HOLD}	10			ns	
MDIO Data Setup Time	T _{SU}	10			ns	
Delay from MDC Rising Edge to MDIO Data Change	T _{DELAY}			300	ns	
MDC Clock Rate	f _{MAX}			2.5	MHz	

Pin Descriptions

Pin#	Symbol	I/O	Logic	Description	Pin#
1	GND	I	Supply	Electrical ground	1
2	GND	I	Supply	Electrical ground	2
3	GND	I	Supply	Electrical ground	3
4	5.0V	I	Supply	Power	4
5	3.3V	I	Supply	Power	5
6	3.3V	I	Supply	Power	6
7	APS	I	Supply	Adaptive Power Supply	7
8	APS	I	Supply	Adaptive Power Supply	8
9	LASI	O	Open Drain	Link Alarm Status Interrupt. 10-22k ohm pull up on host	9
10	REST	I	1.2V CMOS	TX OFF when MDIO RESET	10
11	VEND SPECIFIC			Vendor Specific Pin. Leave unconnected	11
12	TX ON/OFF	I	1.2V CMOS	Transmitter ON/OFF	12
13	Reserved			Reserved	13
14	MOD DETECT	O		Pulled low inside module through 1k ohm	14
15	VEND SPECIFIC			Vendor Specific Pin. Leave unconnected	15
16	VEND SPECIFIC			Vendor Specific Pin. Leave unconnected	16
17	MDIO	I/O	Open Drain	Management Data IO	17
18	MDC	I	1.2V CMOS	Management data clock	18
19	PRTAD4	I	1.2V CMOS	Port Address bit 4 (Low=0)	19
20	PRTAD3	I	1.2V CMOS	Port Address bit 3 (Low=0)	20
21	PRTAD2	I	1.2V CMOS	Port Address bit 2 (Low=0)	21
22	PRTAD1	I	1.2V CMOS	Port Address bit 1 (Low=0)	22
23	PRTAD0	I	1.2V CMOS	Port Address bit 0 (Low=0)	23
24	VEND SPECIFIC			Vendor Specific Pin. Leave unconnected	24
25	APS SET	O		Feedback output for APS	25
26	RESERVED			Reserved for Avalanche Photodiode use	26
27	APS SENSE	O	Analog	APS Sense Connection	27
28	APS	I	Supply	Adaptive Power Supply	28
29	APS	I	Supply	Adaptive Power Supply	29
30	3.3V	I	Supply	Power	30
31	3.3V	I	Supply	Power	31
32	5.0V		Supply	Power	32
33	GND	I	Supply	Electrical Ground	33
34	GND	I	Supply	Electrical Ground	34

35	GND	I	Supply	Electrical Ground	35
36	GND	I	Supply	Electrical Ground	36
37	GND	I	Supply	Electrical Ground	37
38	RESERVED			Reserved	38
39	RESERVED			Reserved	39
40	GND	I	Supply	Electrical ground	40
41	RX LANE 0+	O	AC	Module XAUI Output Lane 0+	41
42	RX LANE 0-	O	AC	Module XAUI Output Lane 0-	42
43	GND	I	Supply	Electrical ground	43
44	RX LANE 1+	O	AC	Module XAUI Output Lane 1+	44
45	RX LANE 1-	O	AC	Module XAUI Output Lane 1-	45
46	GND	I	Supply	Electrical ground	46
47	RX LANE 2+	O	AC	Module XAUI Output Lane 2+	47
48	RX LANE 2-	O	AC	Module XAUI Output Lane 2-	48
49	GND	I	Supply	Electrical ground	49
50	RX LANE 3+	O	AC	Module XAUI Output Lane 3+	50
51	RX LANE 3-	O	AC	Module XAUI Output Lane 3-	51
52	GND	I	Supply	Electrical ground	52
53	GND	I	Supply	Electrical ground	53
54	GND	I	Supply	Electrical ground	54
55	TX LANE 0+	O	AC	Module XAUI Input Lane 0+	55
56	TX LANE 0-	O	AC	Module XAUI Input Lane 0-	56
57	GND	I	Supply	Electrical ground	57
58	TX LANE 1+	O	AC	Module XAUI Input Lane 1+	58
59	TX LANE 1-	O	AC	Module XAUI Input Lane 1-	59
60	GND	I	Supply	Electrical ground	60
61	TX LANE 2+	O	AC	Module XAUI Input Lane 2+	61
62	TX LANE 2-	O	AC	Module XAUI Input Lane 2-	62
63	GND	I	Supply	Electrical ground	63
64	TX LANE 3+	O	AC	Module XAUI Input Lane 3+	64
65	TX LANE 3-	O	AC	Module XAUI Input Lane 3-	65
66	GND	I	Supply	Electrical ground	66
67	RESERVED			Reserved	67
68	RESERVED			Reserved	68
69	GND	I	Supply	Electrical Ground	69
70	GND	I	Supply	Electrical Ground	70

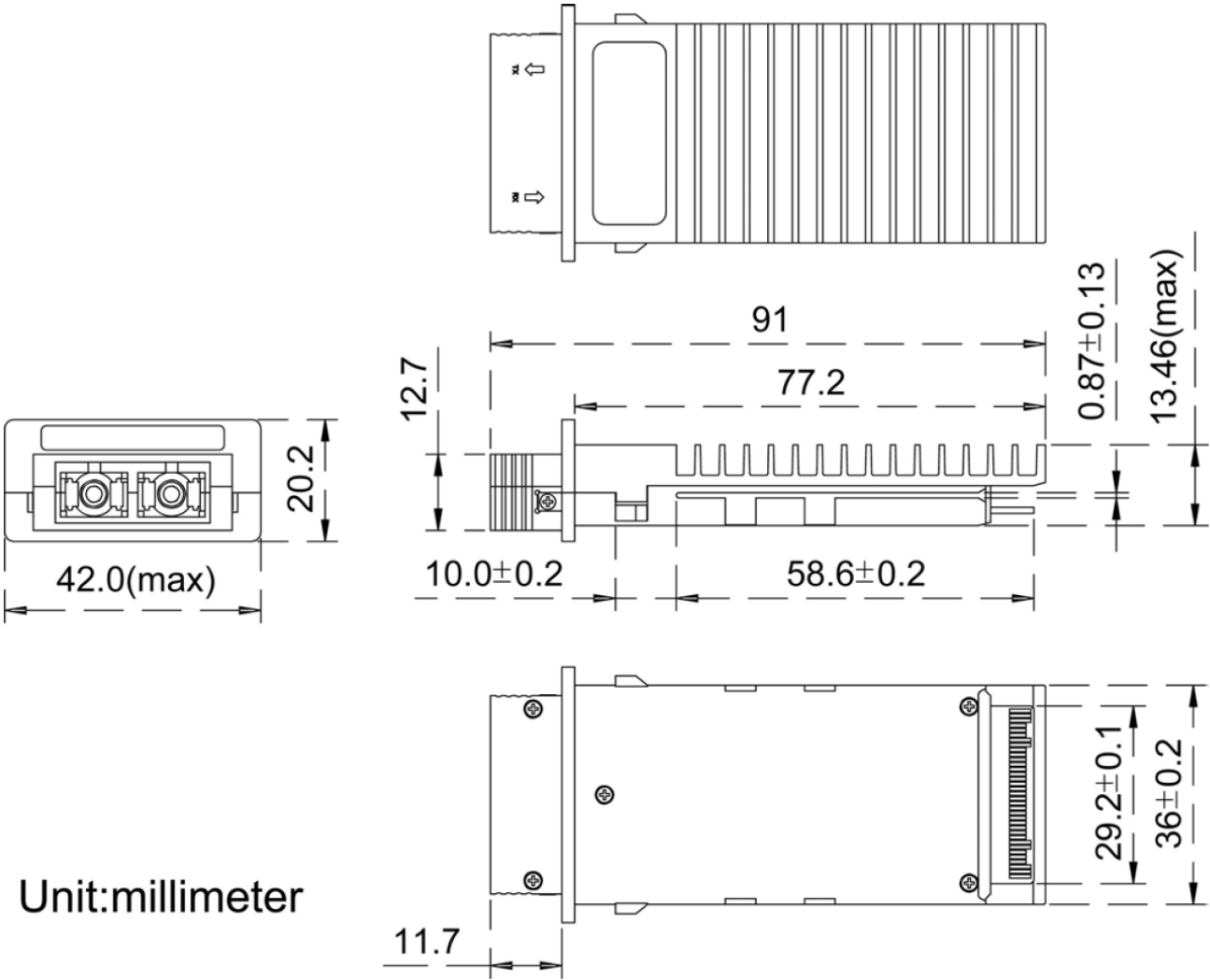
Electrical Pad Layout



Digital Diagnostic

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Temperature Monitor	T _{MON}	-5		5	°C	
Laser Bias Monitor	I _{MON}	-10		10	%	
TX Power Monitor	P _{TX}	-3		3	dBm	
RX Power Monitor	P _{RX}	-3		3	dBm	

Mechanical Specifications



About Skylane Optics

Skylane is a leading provider of transceivers for optical communication.

We offer an extensive portfolio for the enterprise, access, datacenter and metropolitan fiber optical market as well as for smart home applications and home networks.

We cover the European, South American and North American market with a strong partner network and have offices in Belgium, Brazil, Sweden and USA.

Our offerings are characterized by high quality and performance. In combination with our strong technical support, we enable our customers to build cost optimized network solutions.

We offer an extensive range of high-quality products including transceivers (Optical and copper), Active Optical Cable (AOC), Direct Attach Cable (DAC), Mux/Demux, Coding Box.

