



FTLX1672D3BCL-SKY

Finisar® FTLX1672D3BCL Compatible 10GBase-ER SFP+ Transceiver (SMF, 1550nm, 40km, LC, DOM)

Product Description

This Finisar® FTLX1672D3BCL compatible SFP+ transceiver provides 10GBase-ER throughput up to 40km over single-mode fiber (SMF) using a wavelength of 1550nm via an LC connector. It can operate at temperatures between 0 and 70C. Our transceiver is built to meet or exceed OEM specifications and is guaranteed to be 100% compatible with Finisar®. It has been programmed, uniquely serialized, and tested for data-traffic and application to ensure that it will initialize and perform identically. All of our transceivers comply with Multi-Source Agreement (MSA) standards to provide seamless network integration. Additional product features include Digital Optical Monitoring (DOM) support which allows access to real-time operating parameters. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

Skylane's transceivers are RoHS compliant and lead-free.

Features:

- SFF-8432 and SFF-8472 Compliance
- Temperature-stabilized EML transmitter and PIN receiver
- Duplex LC Connector
- Commercial Temperature 0 to 70 Celsius
- Single-mode Fiber
- Hot Pluggable
- Excellent ESD Protection
- Metal with Lower EMI
- RoHS Compliant and Lead Free



Applications:

- 10GBase-ER Ethernet
- Access, Metro and Enterprise
- 8x/10x Fibre Channel

For your product safety, please read the following information carefully before any manipulation of the transceiver:



ESD

This transceiver is specified as ESD threshold 1kV for SFI pins and 2kV for all others electrical input pins, tested per MIL-STD-883G, Method 3015.4 / JESD22-A114-A (HBM). However, normal ESD precautions are still required during the handling of this module.



LASER SAFETY

This is a Class1 Laser Product according to IEC 60825-1:2007. This product complies with 21 CFR 1040.10 and 1040.11 except for deviations pursuant to Laser Notice No. 50, dated (June 24, 2007).

The optical ports of the module need to be terminated with an optical connector or with a dust plug in order to avoid contamination.

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Maximum Supply Voltage	V _{CC}	-0.5	4.0	V
Storage Temperature	T _S	-40	85	°C
Operating Case Temperature	T _C	0	70	°C
Operating Humidity	RH	5	85	%
Receiver Power	R _{MAX}		-1	dBm
Maximum Bitrate	B _{max}		11.3	Gbps

Electrical Characteristics (TOP=25°C, V_{CC}=3.3Volts)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Power Supply Voltage	V _{CC}	3.15	3.30	3.43	V	
Power Supply Current	I _{CC}			303	mA	
Power Consumption	P _{DISS}			1	W	
Transmitter						
Differential data input swing	V _{in,pp}	120		850	mV	
Input differential impedance	Z _{in}	80	100	120	Ω	
Receiver						
Differential data output swing	V _{out, pp}	300		850	mV	
Output differential impedance	Z _{in}	80	100	120	Ω	

Optical Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Notes
Transmitter						
Optical Power (average)	P _{AVE}	-4		4	dBm	1
Optical Modulation amplitude (OMA)	P _{OMA}	-2.1			dBm	
Transmitter and Dispersion Penalty	TDP			2	dB	
Optical Extinction Ratio	ER	8.2			dB	
Optical Wavelength	Tλ	1530	1550	1565	nm	
Insertion loss	IL		0.7			
Receiver						
Receiver Sensitivity (average)	R _{AVE}			-15.8	dBm	3
Receiver Sensitivity (OMA)	R _{OMA}			-14.1		2
Receiver overload	P _{max}	-1			dBm	4
Receiver wavelength	Rλ	1260		1565	nm	

Notes:

1. Coupled into a Single-mode fibre
2. Per IEEE 802.3ae specification
3. Average power, back-to-back, @10.31Gbps, BER 1E-12, PRBS 231-1. TDP not included.
4. Exceeding the Receiver overload can physically damage the module. Please use appropriate attenuation.

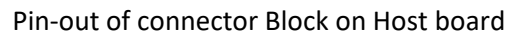
Pin Descriptions

Pin	Symbol	Name/Descriptions	Ref.
1	VeeT	Transmitter Ground (Common with Receiver Ground).	1
2	TX Fault	Transmitter Fault. LVTTTL-O	2
3	TX Disable	Transmitter Disable. Laser output disabled on high or open. LVTT-I.	3
4	SDA	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I/O.	
5	SCL	2-Wire Serial Interface Data Line (Same as MOD-DEF2 in INF-8074i). LVTTTL-I.	
6	MOD_ABS	Module Absent, Connect to VeeT or VeeR in Module.	4
7	RS0	Rate Select 0. Not used	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation. LVTTTL-O.	2
9	RS1	Rate Select 1. Not used	5
10	VeeR	Receiver Ground (Common with Transmitter Ground).	1
11	VeeR	Receiver Ground (Common with Transmitter Ground).	1
12	RD-	Receiver Inverted DATA out. AC Coupled. CML-O.	
13	RD+	Receiver Non-inverted DATA out. AC Coupled. CML-O.	
14	VeeR	Receiver Ground (Common with Transmitter Ground).	1
15	VccR	Receiver Power Supply.	
16	VccT	Transmitter Power Supply.	
17	VeeT	Transmitter Ground (Common with Receiver Ground).	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled. CML-I.	
19	TD-	Transmitter Inverted DATA in. AC Coupled. CML-O.	
20	VeeT	Transmitter Ground (Common with Receiver Ground).	1

Notes:

1. The module signal ground contacts, VeeR and VeeT, should be isolated from the module case.
2. This contact is an open collector/drain output and should be pulled up to the Vcc_Host with resistor in the range 4.7KΩ to 10KΩ. Pull ups can be connected to one or several power supplies, however the host board design shall ensure that no module contract has voltage exceeding module VccT/R +0.5.V.
3. Tx_Disable is an input contact with a 4.7KΩ to 10KΩ pull-up resistor to VccT inside module.
4. Mod_ABS is connected to VeeT or VeeR in the SFP+ module. The host may pull the contract up to

5. Internally pulled down per SFF-8431

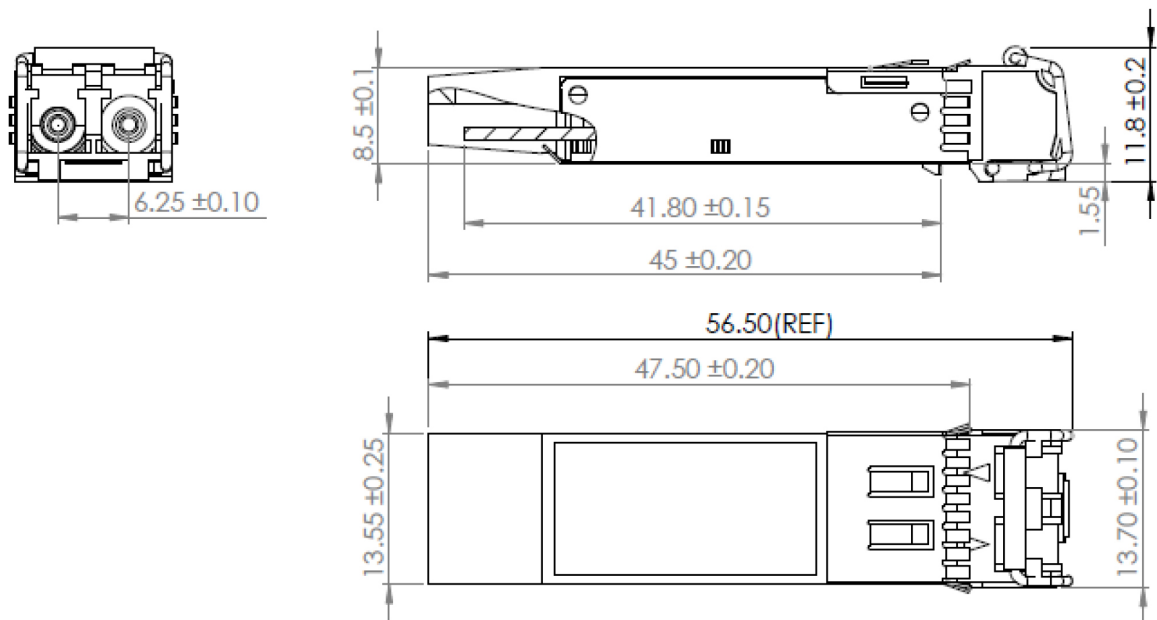


The schematic diagram illustrates the internal architecture and pin connections of the SerDes IC. Key components and connections include:

- LD Driver:** Connected to the Tx data lines (TD+, TD-) through a 100Ω resistor.
- POST Amp:** Connected to the Rx data lines (RD+, RD-) through a 100Ω resistor.
- EEPROM:** Connected to the MOD_ABS pin (pin 6) and the SCL/SDA pins (pins 5 and 4).
- Control Pins:**
 - Tx_DISABLE (pin 3):** Connected to the Tx_DISABLE pin of the Protocol IC.
 - Tx_FAULT (pin 2):** Connected to the Tx_FAULT pin of the Protocol IC.
 - LOS (pin 8):** Connected to the LOSS OF SIGNAL pin of the Protocol IC.
 - MOD_ABS (pin 6):** Connected to the MOD_ABS pin of the Protocol IC.
 - SCL (pin 5):** Connected to the SCL pin of the Protocol IC.
 - SDA (pin 4):** Connected to the SDA pin of the Protocol IC.
- Power Supply:**
 - VccT (pin 16):** Connected to the VccT pin of the Protocol IC.
 - VccR (pin 15):** Connected to the VccR pin of the Protocol IC.
- Data Lines:**
 - TD+ (pin 18):** Connected to the TD+ pin of the Protocol IC.
 - TD- (pin 19):** Connected to the TD- pin of the Protocol IC.
 - RD+ (pin 13):** Connected to the RD+ pin of the Protocol IC.
 - RD- (pin 12):** Connected to the RD- pin of the Protocol IC.
- Resistors:** 10KΩ, 30KΩ, 50KΩ, 100KΩ, and 100Ω resistors are used for various pull-up and pull-down functions.
- Capacitors:** 0.1μF and 22μF capacitors are used for decoupling and timing.
- Inductors:** 4.7μH inductors are used for impedance matching.

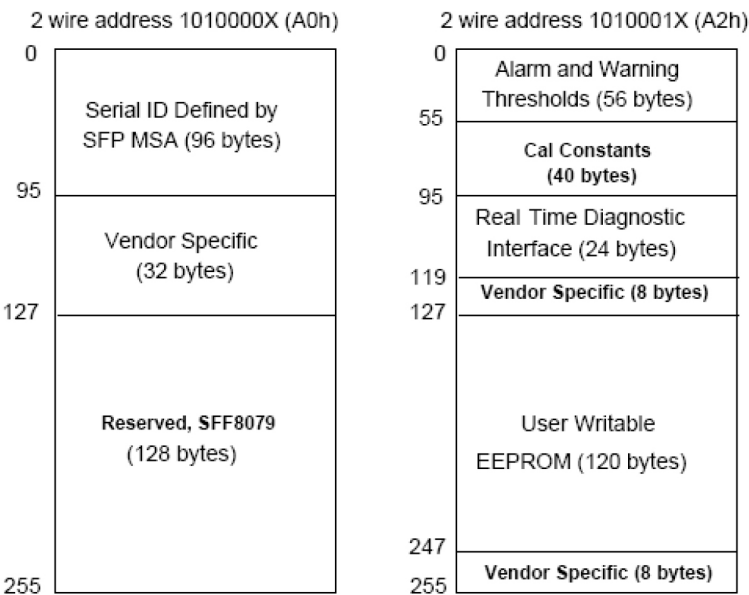
Mechanical Specifications

Small Form Factor Pluggable (SFP) transceivers are compatible with the dimensions defined by the SFP Multi-Sourcing Agreement (MSA).



EEPROM Information

EEPROM memory map specific data field description is as below:



About Skylane Optics

Skylane is a leading provider of transceivers for optical communication.

We offer an extensive portfolio for the enterprise, access, datacenter and metropolitan fiber optical market as well as for smart home applications and home networks.

We cover the European, South American and North American market with a strong partner network and have offices in Belgium, Brazil, Sweden and USA.

Our offerings are characterized by high quality and performance. In combination with our strong technical support, we enable our customers to build cost optimized network solutions.

We offer an extensive range of high-quality products including transceivers (Optical and copper), Active Optical Cable (AOC), Direct Attach Cable (DAC), Mux/Demux, Coding Box.

